



FORM PTO-1449

INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO.

1778.0220000

APPLICATION NO.

10/698,061

INVENTORS

Banerjee *et al.*

FILING DATE

October 31, 2003

ART UNIT

2183

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
DL	AA1	3,543,245	11/1970	Nutter			
DL	AB1	3,631,405	12/1971	Hoff <i>et al.</i>			
DL	AC1	3,949,372	04/1976	Brioschi			
DL	AD1	4,077,058	02/1978	Appell <i>et al.</i>			
DL	AE1	4,084,235	04/1978	Hirtle <i>et al.</i>			
DL	AF1	4,274,138	06/1981	Shimokawa			
DL	AG1	4,575,797	03/1986	Gruner <i>et al.</i>			
DL	AH1	4,774,652	09/1988	Dhuey <i>et al.</i>			
DL	AI1	4,782,443	11/1988	Matsumoto			
DL	AJ1	4,835,734	05/1989	Kodaira <i>et al.</i>			

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
DL	AK1	0 109 567 A2	05/1984	EP			N/A
DL	AL1	0 169 565 A2	01/1986	EP			N/A
DL	AM1	0 199 173 A2	10/1986	EP			N/A

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DL	AN	1	Bandyopadhyay, A. and Zheng, Y.F., "Combining Both Micro-Code And Hardwired Control In RISC," 5 pages, (published in ACM SIGARCH Computer Architecture News, Vol. 15, Issue 4, pp. 11-15 (September 1987)).				
DL	AO	1	Hayashi, T. <i>et al.</i> , "A 5.6-MIPS Call-Handling Processor for Switching Systems," <i>IEEE Journal of Solid-State Circuits</i> , Vol. 24, No. 4, IEEE, pp. 945-950 (August 1989).				
DL	AP	1	"High Performance Dual Architecture Processor," <i>IBM Technical Disclosure Bulletin</i> , Vol. 36, No. 2, IBM Corp., pp. 231-234 (February 1993).				
DL	AQ	1	McNeley, K.J. and Milutinovic, V.M., "Emulating a Complex Instruction Set Computer with a Reduced Instruction Set Computer," <i>IEEE Micro</i> , IEEE, pp. 60-72 (February 1987).				
DL	AR	1	Sweetman, D., <i>See MIPS Run</i> , Morgan Kaufman Publishers, Inc., ISBN 1-55860-410-3, pp.vii-xiv and 423-425 (1999).				

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<i>DL</i>	AA2	4,839,797	06/1989	Katori <i>et al.</i>			
<i>DL</i>	AB2	4,868,740	09/1989	Kagimasa <i>et al.</i>			
<i>DL</i>	AC2	4,876,639	10/1989	Mensch, Jr.			
<i>DL</i>	AD2	5,193,158	03/1993	Kinney <i>et al.</i>			
<i>DL</i>	AE2	5,280,593	01/1994	Bullions, III <i>et al.</i>			
<i>DL</i>	AF2	5,307,504	04/1994	Robinson <i>et al.</i>			
<i>DL</i>	AG2	5,335,331	08/1994	Murao <i>et al.</i>			
<i>DL</i>	AH2	5,420,992	05/1995	Killian <i>et al.</i>			
<i>DL</i>	AI2	5,438,668	08/1995	Coon <i>et al.</i>			
<i>DL</i>	AJ2	5,463,700	10/1995	Nakazawa			

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
<i>DL</i>	AK2	0 324 308 A2	07/1989	EP			N/A
<i>DL</i>	AL2	WO 95/30187 A1	11/1995	WO			N/A
<i>DL</i>	AM2	WO 95/30188 A1	11/1995	WO			N/A

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

<i>DL</i>	AN	2	Turley, J., "LSI's TinyRisc Core Shrinks Code Size: Code-Compaction Technique Squeezes MIPS Instructions Into 16 Bits," <i>Microprocessor Report</i> , Microdesign Resources, pp. 40-43 (October 28, 1996).
	AO	2	
	AP	2	
	AQ	2	
	AR	2	

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2183**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
<i>DE</i>	AA3	5,467,134	11/1995	Laney <i>et al.</i>			
<i>DE</i>	AB3	5,481,684	01/1996	Richter <i>et al.</i>			
<i>DE</i>	AC3	5,481,693	01/1996	Blomgren <i>et al.</i>			
<i>DE</i>	AD3	5,519,873	05/1996	Butter <i>et al.</i>			
<i>DE</i>	AE3	5,542,059	07/1996	Blomgren			
<i>DE</i>	AF3	5,546,552	08/1996	Coon <i>et al.</i>			
<i>DE</i>	AG3	5,568,646	10/1996	Jaggar			
<i>DE</i>	AH3	5,574,887	11/1996	Fitch			
<i>DE</i>	AI3	5,574,927	11/1996	Scantlin			
<i>DE</i>	AJ3	5,574,928	11/1996	White <i>et al.</i>			

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
<i>DE</i>	AK3	WO 96/24895 A1	08/1996	WO			N/A
<i>DE</i>	AL3	2 016 755 A	09/1979	GB			N/A
<i>DE</i>	AM3	2 284 492 A	06/1995	GB			N/A

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AN	3	
	AO	3	
	AP	3	
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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
<i>DC</i>	AA4	5,598,546	01/1997	Blomgren			
<i>DC</i>	AB4	5,619,665	04/1997	Emma			
<i>DC</i>	AC4	5,619,666	04/1997	Coon <i>et al.</i>			
<i>DC</i>	AD4	5,632,024	05/1997	Yajima <i>et al.</i>			
<i>DC</i>	AE4	5,638,525	06/1997	Hammond <i>et al.</i>			
<i>DC</i>	AF4	5,652,852	07/1997	Yokota			
<i>DC</i>	AG4	5,664,136	09/1997	Witt <i>et al.</i>			
<i>DC</i>	AH4	5,685,009	11/1997	Blomgren <i>et al.</i>			
<i>DC</i>	AI4	5,740,461	04/1998	Jaggar			
<i>DC</i>	AJ4	5,758,115	05/1998	Nevill			

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
<i>DC</i>	AK4	2 289 353 A	11/1995	GB			N/A
<i>DC</i>	AL4	2 290 395 A	12/1995	GB			N/A
	AM4						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AN	4	
	AO	4	
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	AQ	4	
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<i>DE</i>	AA5	5,774,686	06/1998	Hammond <i>et al.</i>			
<i>DE</i>	AB5	5,781,750	07/1998	Blomgren <i>et al.</i>			
<i>DE</i>	AC5	5,794,010	08/1998	Worrell <i>et al.</i>			
<i>DE</i>	AD5	5,796,973	08/1998	Witt <i>et al.</i>			
<i>DE</i>	AE5	5,828,859	10/1998	Tanihira <i>et al.</i>			
<i>DE</i>	AF5	5,854,913	12/1998	Goetz <i>et al.</i>			
<i>DE</i>	AG5	5,867,681	02/1999	Worrell <i>et al.</i>			
<i>DE</i>	AH5	5,867,682	02/1999	Witt <i>et al.</i>			
<i>DE</i>	AI5	5,896,519	04/1999	Worrell			
<i>DE</i>	AJ5	5,905,893	05/1999	Worrell			

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	AK5						Yes No
	AL5						Yes No
	AM5						Yes No

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	AN	5	
	AO	5	
	AP	5	
	AQ	5	
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<i>DL</i>	AA6	5,954,830	09/1999	Temullo, Jr.			
<i>DL</i>	AB6	6,012,138	01/2000	Worrell			
<i>DL</i>	AC6	6,021,265	02/2000	Nevill			
<i>DL</i>	AD6	6,651,160 B1	11/2003	Hays			09/01/2000
	AE6						
	AF6						
	AG6						
	AH6						
	AI6						
	AJ6						

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	AK6						Yes No
	AL6						Yes No
	AM6						Yes No

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